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Substitute for form 1449/PTO

Sheet 1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number 10/781,166

Filing Date February 18, 2004

First Named Inventor Richard W. Foote

Art Unit 2829

Examiner Name Not Yet Assigned

Attorney Docket Number P05810

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	AB	^{US-} 4,413,272	11/01/1983	Mochizuki et al.	
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Examiner Initials*	Cite No.1	Foreign Patent Document	Publication	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	
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Substitute for form 1449/PTO	Complete if Known			
	Application Number	10/781,166		
INFORMATION DISCLOSURE	Filing Date	February 18, 2004		
	First Named Inventor	Richard W. Foote		
STATEMENT BY APPLICANT	Art Unit	2829		
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, senal, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
77/	. CA	Simon S. Cohen et al., "Laser-Induced Melting of Thin Conducting Films: Part I-The Adiabatic Approximation", IEEE Transactions on Electron Devices, Vol. 38, No. 9, September 1991, Pp. 2042-2050.	
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	CD	Will R. Moore, "A Review of Fault-Tolerant Techniques for the Enhancement of Integrated Circuit Yield", Proceedings of the IEEE, Vol. 74, No. 5, May 1986, Pp. 684-698.	
	CE	Yunlong Sun et al., "Optimization of Memory Redundancy Laser Link Processing", SPIE Vol. 2636, Pp. 152-164.	
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